

PROTECTIVE ARRANGEMENT FOR A SEMICONDUCTOR CIRCUIT SYSTEM HAVING A THYRISTOR STRUCTURE, AND METHOD FOR THE OPERATION THEREOF

Publication number: DE102004029008 (A1)

Also published as:

WO2005124863 (A1)

Publication date: 2006-01-05

Cited documents:

Inventor(s): MAYERHOFER MICHAEL [AT]; DEUTSCHMANN BERND [AT]; FANKHAUSER BERND [AT]

US5982601 (A)

Applicant(s): AUSTRIAMICROSYSTEMS AG UNTERPR [AT]

Classification:

- international: H01L23/60; H01L27/02; H01L27/082; H01L23/58;
H01L27/02; H01L27/082

- European: H01L27/02B4F4

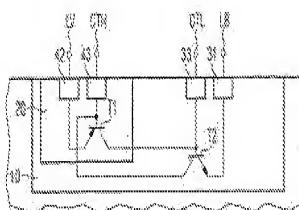
Application number: DE200410029008 20040616

Priority number(s): DE200410029008 20040616

Abstract not available for DE 102004029008 (A1)

Abstract of corresponding document: WO 2005124863 (A1)

Disclosed is a protective arrangement for a semiconductor circuit system having a thyristor structure (SCR) which is disposed in a first trough (10) having a first type of conductivity and a second trough (20) that has a second type of conductivity and is embedded in said first trough (10). The first trough encompasses a highly doped region (11; 31) that has the second type of conductivity and is connected to a first potential (VB), and a first control region (13; 33) having the first type of conductivity. The second trough is provided with a highly doped region (22; 42) that has the first type of conductivity and is connected to a second potential (VV), and a second control region (23; 43) having the second type of conductivity. The first and second control region are connected to a first and a second control potential (CTL, CTH), respectively.



Data supplied from the esp@cenet database — Worldwide